

Spring 2017

Course	Course Title	Component	Days	Beginning Time	Ending Time	Building	Room	Faculty	
Undergraduate									
ESE	121	Introduction to Audio Systems	LEC	TUTH	10:00 AM	11:20 AM	HUMAN	1006	Jan Folkson
ESE	123	Intro to Elec and Compu Engrng	LEC	MW	5:30 PM	6:50 PM	LIBRARY	E4330	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	M	12:45 PM	3:45 PM	LGT ENGR	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	TU	11:30 AM	2:30 PM	LGT ENGR	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	TH	11:30 AM	2:30 PM	LGT ENGR	283	David Westerfeld
ESE	124	Computer Tech for Elect Dsgn I	LEC	TUTH	1:00 PM	2:20 PM	ENGR	145	Jayant Parekh
ESE	211	Electronics Laboratory A	LEC	M	10:00 AM	10:53 AM	LGT ENGR	102	Sergey Suchalkin
ESE	211	Electronics Laboratory A	LAB	W	10:00 AM	1:00 PM	LGT ENGR	283	Sergey Suchalkin
ESE	211	Electronics Laboratory A	LAB	W	7:00 PM	10:00 PM	LGT ENGR	283	Sergey Suchalkin
ESE	211	Electronics Laboratory A	LAB	TH	2:30 PM	5:30 PM	LGT ENGR	283	Sergey Suchalkin
ESE	218	Digital Systems Design	LEC	TUTH	5:30 PM	6:50 PM	TBA	TBA	Dmitri Donetski
ESE	218	Digital Systems Design	LAB	M	8:55 AM	11:55 AM	LGT ENGR	1111	Dmitri Donetski
ESE	218	Digital Systems Design	LAB	M	7:00 PM	10:00 PM	LGT ENGR	283A	Dmitri Donetski
ESE	218	Digital Systems Design	LAB	TU	7:00 PM	10:00 PM	LGT ENGR	283A	Dmitri Donetski
ESE	224	Cmptr Tchnq for Electr Dsgn II	LEC	TUTH	10:00AM	11:20AM	LGT ENGR	122	Vibha Mane
ESE	231	Intro to Semiconductor Devices	LEC	MW	2:30 PM	3:50 PM	FREY HALL	205	Ridha Kamoua
ESE	271	Electrical Circuit Analysis I	LEC	TUTH	2:30 PM	3:50 PM	FREY HALL	317	Jayant Parekh
ESE	271	Electrical Circuit Analysis I	REC	TU	11:00 AM	11:53 AM	FREY HALL	216	Jayant Parekh
ESE	271	Electrical Circuit Analysis I	REC	TH	11:00 AM	11:53 AM	FREY HALL	216	Jayant Parekh

ESE	300	Tech Commun for EE/CE	LEC	W	7:00 PM	10:00 PM	MELVILLE L	W455	Ronald Marge
ESE	301	Eng Ethics and Soc Impact	LEC	TUTH	8:30 AM	9:50 AM	MELVILLE L	W455	Donna Tumminello
ESE	304	Apps of Operational Amplifiers	LEC	TUTH	1:00 PM	2:20 PM	HARMIN	112	Mikhail Gouzman
ESE	305	Deterministic Signals & Systms	LEC	TUTH	1:00 PM	2:20 PM	FREY HALL	317	Shan Lin
ESE	306	Random Signals & Systems	LEC	MW	5:30 PM	6:50 PM	LGT ENGR	102	Vibha Mane
ESE	306	Random Signals & Systems	REC	RECM	11:00 AM	11:53 AM	ENGR	143	Petar Djuric
ESE	311	Analog Integrated Circuits	LEC	MW	2:30 PM	3:50 PM	CHEM	128	Dmitri Donetski
ESE	313	Introduction to Photovoltaics	LEC	M	7:00 PM	10:00 PM	LGT ENGR	152	Matthew Eisaman
ESE	324	Electronics Laboratory C	LEC	TH	4:00 PM	4:53 PM	MELVILLE L	W455	Emre Salman
ESE	324	Electronics Laboratory C	LAB	TU	5:30 PM	8:30 PM	LGT ENGR	283	Emre Salman
ESE	324	Electronics Laboratory C	LAB	M	7:00 PM	10:00 PM	LGT ENGR	283	Emre Salman
ESE	324	Electronics Laboratory C	LAB	TH	7:00 PM	10:00 PM	LGT ENGR	283	Emre Salman
ESE	333	Real-Time Operating Systems	LEC	TUTH	10:00 AM	11:20 AM	FREY HALL	313	Fan Ye
ESE	342	Digital Communications Systems	LEC	MW	8:30 AM	9:50 AM	CHEM	126	Monica Bugallo
ESE	343	Mobile Cloud Computing	LEC	TU	4:00 PM	7:00 PM	FREY HALL	105	Shan Lin
ESE	344	Software Techniques for Engnrs	LEC	TUTH	11:30 AM	12:50 PM	CHEM	126	Murali Subbarao
ESE	346	Computer Communications	LEC	TUTH	11:30 AM	12:50 PM	HUMAN	1003	Thomas Robertazzi
ESE	347	Digital Signal Process: Implem	LEC	MW	5:30 PM	6:50 PM	SOC BEH	N102	Dmitri Gavrillov
ESE	347	Digital Signal Process: Implem	LAB	M	7:00 PM	10:00 PM	LGT ENGR	179	Dmitri Gavrillov
ESE	350	Electrical Power Systems	LEC	M	2:30 PM	5:30 PM	LGT ENGR	154	Timothy Driscoll

ESE	360	Network Security Engineering	LEC	TU	6:00 PM	9:00 PM	FREY HALL	205	Carlos Gamboa
ESE	372	Electronics	LEC	MW	4:00 PM	5:20 PM	LGT ENGR	102	Leon Shterengas
ESE	372	Electronics	REC	RETU	4:00 PM	5:20 PM	FREY HALL	217	Leon Shterengas
ESE	372	Electronics	REC	RETH	1:00 PM	2:20 PM	E &SSCI	69	Leon Shterengas
ESE	381	Embedded Microproc Syst Des II	LEC	TUTH	10:00 AM	11:20 AM	LGT ENGR	154	Kenneth Short
ESE	381	Embedded Microproc Syst Des II	LAB	W	9:00 AM	12:00 PM	LGT ENGR	230	Kenneth Short
ESE	381	Embedded Microproc Syst Des II	LAB	W	2:30 PM	5:30 PM	LGT ENGR	230	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LEC	TUTH	2:30 PM	3:50 PM	LGT ENGR	102	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LAB	W	9:00 AM	12:00 PM	LGT ENGR	230	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LAB	W	2:30 PM	5:30 PM	LGT ENGR	230	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LAB	TH	10:00 AM	1:00 PM	LGT ENGR	230	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LAB	F	10:00 AM	1:00 PM	LGT ENGR	230	Kenneth Short
ESE	441	Engineering Design II	LEC	TU	4:00 PM	5:20 PM	LECCTTR	101	Wendy Tang